EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S520	5689	S519 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S519	8219	S518 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S518	25214	S517 and ("same time" together jointly simultaneous$3 parallal concurrent$3 simultaneity synchronic synchronous$3) with test$4	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S517	130532	S511 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S516	61	S505 same (simultaneous$3 parallal concurrent$3 simultaneity synchronic synchronous$3) same test$3 same S511	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S515	1	"7266746".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S514	103	S513 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S513	275	S506 and S512	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S512	6133	S501 same S511	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S511	2823818	(((back backward forward) near2 trac$3) (trace tracing) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing) cone cones subgroup group groups (logic near2 path) (logic group))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S510	115	S509 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S509	170	S507 and S508	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S508	119047	S497 same (group$4 set sets cluster collection number plurality bunch$3 band$3 match$4)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S507	360	S506 and S497	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S506	1212	S501 and S505	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S505	27343	((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S504	1	"20040073859".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S503	40	S502 and S499	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S502	3269	S500 and S501	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S501	52684	(simultaneous$3 parallal concurrent$3 simultaneity synchronic synchronous$3) with test$4	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S500	114979	S497 same (group$4 set sets cluster collection number plurality bunch$3 band$3)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S499	479	S498 and test$4	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S498	611	S496 same S497	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S497	381840	(((back backward forward) near2 trac$3) (trace tracing) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S496	9407	(net-list netlist net?list (net adj list))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S495	265	S491 and S494	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S494	32409	S492 S316	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:52
S493	30246	S315 with ((cost importance value weight significan$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:52
S492	3590	S315 with (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:52
S491	1869	(S488 with S315) and S490	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S490	73622	(netlist net-list (net adj list) (description near2 language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:52

S489	1135915	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:52
S488	40827	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:52
S487	40	S478 and S486	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S486	1869	S484 or S485	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S485	68	(S470 with S472) and S471	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S484	1869	(S470 with S471) and S472	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S483	31347	((S470 same S471) (S470 same S472)) and (S472 S471)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S482	154	S470 same S471 same S472	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S481	255	S474 and S480	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S480	23041	S471 with ((cost importance value weight significan$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S479	40	S474 and S478	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S478	2388	S471 with (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S477	103	S475 and S476	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S476	7219	S471 same (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S475	570	S474 and (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S474	1869	S473 and S472	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S473	31347	S470 with S471	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S472	52412	(netlist net-list (net adj list) (description near2 language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S471	667945	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S470	31347	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S469	8	S468 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S468	61	S466 and (S436 same (cost importance weight significan$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S467	259	S466 and (cost importance weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S466	342	S458 and S465	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S465	272104	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) with (cost importance (new near2 design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S464	27	S463 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S463	242	S460 and (S436 same (cost importance value weight significan$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S462	364	S460 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S461	1	S460 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S460	382	S458 and S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S459	142	S458 same S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S458	539	S442 with S434	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S457	655	S447 and (S436 same (cost importance value weight significan$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S456	1094	S447 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S455	510	S444 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S454	0	S451 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S453	15	S452 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S452	130	S451 and @ad<="20021009"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S451	236	S444 same (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S450	510	S444 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S449	1	S448 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S448	124	S447 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S447	1137	S443 and S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S446	1	S443 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S445	42	S444 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S444	520	S443 same S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S443	1560	S442 same S434	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S442	5501888	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog (funcational adj configuration))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S441	42	S440 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S440	520	S438 same S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S439	1137	S438 and S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S438	1560	S432 same S434	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S437	21237	S435 and S436	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S436	316456	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) with (cost importance value (new near2 design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S435	31028	S432 and S434	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S434	31347	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S433	50610	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S432	5501888	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S431	4	S430 and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S430	19	(test adj equipment adj platform)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S429	10	S428 and ("714"/$.ccls.)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S428	883	S427 and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S427	1457	TEP (test adj equipment adj platform)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S426	42	S424 and S410	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S425	128	S424 and S409	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S424	220	S423 and S408	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S423	1200	(environment near3 (operation operating operational normal funcational)) and ((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) and ("714"/$.ccls.) and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S422	2718	(environment near3 (operation operating operational normal funcational)) and ("714"/$.ccls.) and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S421	34725	(test$4 verif$4) with (new$2 additional added) with (unit chip device module circuit)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S420	1	"20020124217".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S419	6	("6256759" "6385750" "5450414" "5592493" "6708305" "5831996").pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S418	66	S414 and ((test$4 verif$4 validat$4 cost magnitude) near2 (cost weight priority control$3 observation))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S417	482	S414 and (cost priority control $3 observation)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S416	54	S414 and ((test$4 verif$4 validat$4) near2 (cost weight priority control$3 observation))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S415	482	S414 and (cost weight priority control$3 observation)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S414	500	S413 and S412	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S413	20784	S408 and S409	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S412	500	(function program algorithm equation code) same S411	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S411	1560	S408 same S410	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S410	31347	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S409	145229	(requirement condition$3 limit $5 criteria rule) same (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S408	50610	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S407	97	validation adj cost	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S406	482	S405 and (cost weight priority control$3 observation)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S405	500	S404 and S392	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S404	20784	S381 and S382	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S403	31	S400 and priority	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S402	23	S383 and S400	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S401	85	S381 and S400	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S400	203	S399 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S399	318	714/741.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S398	34	(S383) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S397	3	(S392 S388) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S396	3	(S393 S390) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S395	17	(S393 S390) and simulation	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S394	17	S393 not S390	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S393	56	S392 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S392	500	(function program algorithm equation code) same S387	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S391	14	("4204633" "4779273" "5390193" "5657240" "5726996" "5831996" "5862149" "5896401" "6215327" "6442722" "6463560" "6467058" "6477684" "6611933").pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S390	39	S388 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S389	234	S388 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S388	400	(function program algorithm) same S387	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S387	1560	S381 same S386	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S386	31347	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S385	63123	(requirement condition$3 limit $5 criteria rule) with (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S384	3056	(function program algorithm) same S383	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S383	9101	S381 same S382	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S382	145229	(requirement condition$3 limit $5 criteria rule) same (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S381	50610	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S380	25	S367 same ((ciritical adj point) (observation adj point) ((target specific particular) near2 (circuit chip module divice unit apparatus)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S379	270	S378 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S378	411	S367 and ((ciritical adj point) (observation adj point) ((target specific particular) near2 (circuit chip module divice unit apparatus)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S377	450	S367 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S376	1	"20050149804".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S375	22	S367 same ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S374	265	S373 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S373	401	S367 and ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S372	34	S371 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S371	50	S367 and (BDD (binary near2 (decision determination determin$3 solution)) (state adj diagram))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S370	87	S369 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S369	138	S367 and S368	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S368	13178	(test$4 verif$4) with ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S367	678	(netlist net-list (net adj list) (description adj language) HDL HTL RTL VHDL verilog) with (((test adj (pattern sequence vector)) (((input near2 output) input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S366	43	S365 same ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S365	2056	(netlist net-list (net adj list) (description adj language) HDL HTL RTL VHDL verilog) with (((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S364	49	S362 and priority	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S363	0	S362 same priority	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S362	147	S360 same ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S361	4355	S360 and ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S360	15686	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) with ((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S359	1274	S357 same ((condition limit$4 constraint paramiter provision circumstances circumstanc requirement$4 limitation restraint) near5 (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data))))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S358	5329	S357 and ((condition limit$4 constraint paramiter provision circumstances circumstanc requirement$4 limitation restraint) near5 (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data))))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S357	50610	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S356	1	"5542043".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S355	1	"5870590".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S354	14460	S353 and (binary)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S353	50610	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S352	7	S348 and ((test test$4 verify verif$4) near2 (pattern sequence vector))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S351	53	S350 and ((resource device circuit element) near2 (constraint restriction limitation constrain$4 restrict$4 limit$4))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S350	201	(validate validating validation) and (functional near2 device) and ((test test$4 verify verif$4) near2 (pattern sequence vector))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S349	897	(((functional configuration) near2 (information data)) and (condition)) and (validation near2 (item unit circuit module chip device))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S348	41	(((functional configuration) near2 (information data)) same (condition)) and (validation near2 (item unit circuit module chip device))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S347	176646	(configuration same condition)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S346	1	"6578167".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S345	155	S344 and "714"/$.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S344	1272	S342 and ((scan same (figure diagram picture graph)) same (input data) same (edge edges node nodes))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S343	33897	S342 and (scan same (figure diagram picture))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S342	4011977	@ad<="20021009"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S341	0	"19990318204".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S340	0	"9990318204".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S339	1	"5394347".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S338	1	"6694290".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S337	1	"5870590".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S336	1	"6631340".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S335	96	S333 and "714"/$.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S334	876	S333 and (condition$3 limit$4)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S333	876	S332 and (condition$3 limit$4)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S332	897	S331 and (known near3 (good working)) and (together assemble build assemble) and (test check) and (simulat$4)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

S331	4011977	@ad<="20021009"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S330	331	S329 and "714"/$.ccls.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S329	5307	S328 and (condition$3 limit$4)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S328	5598	S324 and (known near3 (good working)) and (together assemble build assemble) and (test simulate check)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S327	1	"5918037".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S326	0	"8658344".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S325	0	"08658344".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S324	4011977	@ad<="20021009"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S323	2	("5542043" "5870590").pn.	USPAT	OR	ON	2009/01/29 14:52
S322	2	"20020138802"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S321	1	"20040073859"	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(((back backward forward) near2 trac$3) (trace tracing) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing) (logic near2 path) (logic group))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S272 and (input in-put in?put inputting in-putting in?putting) same (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forwadtrace) same (input and output)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S271 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back?tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

		backward-trace) (logic near2 path) (logic near2 group) spread spreading) and (test$4 near2 (pattern vector sequence data program script))				
		S269 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		spreading and input and ouput	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		"5592493".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S261 and S262	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forward adj trace) same input same output	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forward adj trace)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forward near2trace)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forwardtrace)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forwardtrace) same (input same output)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		((generat$4 creat$4) near2 (test $4 near2 (pattern vector sequence data program script)))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S268 and S257	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		((((back backward forward) near2 trac$3) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing back?trace forward?trace backward?trace back-trace forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		"6385750".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(S258 and path)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S257 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

		S257 same path same (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) same (input in-put in?put inputting in-putting in?putting inputs))and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) same (input in-put in?put inputting in-putting in?putting inputs)) and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		"5719881".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52

		((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) same (input in-put in?put inputting in-putting in?putting inputs) same (cone shaded)) and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(backtrace)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S274 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S259 and S260	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		"5592493".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		"5831996".pn.	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		spreading same cone	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(forwadtrace) same (input same output)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		(backtrace) same input and output	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S279 not spectrum	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		S278 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
		((binary near2 decision near2 (graph diagram)) (binary near2 tree)) same (boolean)	US-PGPUB; USPAT	OR	ON	2009/01/29 14:52
S320	83	S314 and S319	US-PGPUB; USPAT	OR	ON	2009/01/29 14:35
S319	7947	S315 with ((cost importance weight significan$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 14:35
S318	306	S314 and S316	US-PGPUB; USPAT	OR	ON	2009/01/29 12:45

S316	30246	S315 with ((cost importance value weight significan$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 12:44
S315	1135915	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/29 12:44
S314	1914	S313 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2009/01/29 12:43
S313	9851	S311 and S312	US-PGPUB; USPAT	OR	ON	2009/01/29 12:40
S312	184490	((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading cost control $4) with ((generat$4 creat$4) near3 (pattern vector sequence data program script item function)))	US-PGPUB; USPAT	OR	ON	2009/01/29 12:40
S311	24196	(((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL configuration ((configuration functional) near2 (information file list out $line outline))) with ((generat $4 creat$4) near3 (pattern vector sequence data program script item function)))	US-PGPUB; USPAT	OR	ON	2009/01/29 12:39
S310	31806	S308 and S309	US-PGPUB; USPAT	OR	ON	2009/01/29 12:39

S309	304212	((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading cost control $4) same ((generat$4 creat$4) near3 (pattern vector sequence data program script item function)))	US-PGPUB; USPAT	OR	ON	2009/01/29 12:38
S308	58328	(((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL configuration ((configuration functional) near2 (information file list out $line outline))) same ((generat $4 creat$4) near3 (pattern vector sequence data program script item function)))	US-PGPUB; USPAT	OR	ON	2009/01/29 12:37
S307	75968	(((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL configuration (configuration near2 (information file list out$line outline))) and ((generat$4 creat $4) near3 (pattern vector sequence data program script item function))) and S306	US-PGPUB; USPAT	OR	ON	2009/01/29 12:35
S306	590416	(((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group)	US-PGPUB; USPAT	OR	ON	2009/01/29 12:34

		spread spreading)				
S305	77	(S289 S285) and S304	US-PGPUB; USPAT	OR	ON	2009/01/29 12:22
S304	4585	((((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL configuration (configuration near2 (information file list out$line outline))) same ((generat$4 creat$4) near3 (pattern vector sequence data program script item function))).clm.	US-PGPUB; USPAT	OR	ON	2009/01/29 12:22
S303	137	(S289 S285) and S302	US-PGPUB; USPAT	OR	ON	2009/01/29 12:11
S302	12786	S298 and S301	US-PGPUB; USPAT	OR	ON	2009/01/29 12:11
S301	299252	(((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL configuration (configuration near2 (information file list out$line outline))).clm.	US-PGPUB; USPAT	OR	ON	2009/01/29 12:11
S299	3150	(S289 S285) and S298	US-PGPUB; USPAT	OR	ON	2009/01/29 12:08
S298	222249	((generat$4 creat$4) near3 (pattern vector sequence data program script item function)).clm.	US-PGPUB; USPAT	OR	ON	2009/01/29 12:08
S297	59	(S289 S285) and S292 and S294	US-PGPUB; USPAT	OR	ON	2009/01/29 12:04
S296	7	(S289 S285) and S292 and S294 and S295	US-PGPUB; USPAT	OR	ON	2009/01/29 12:04
S295	497	((binary near2 decision near2 (graph diagram)) (binary near2 tree)) same (boolean)	US-PGPUB; USPAT	OR	ON	2009/01/29 12:03
S294	45637	(((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL)	US-PGPUB; USPAT	OR	ON	2009/01/29 12:03
S293	1	S286 and S292	US-PGPUB; USPAT	OR	ON	2009/01/29 12:02
S292	16453	((generat$4 creat$4) near2 (test $4 near2 (pattern vector sequence data program script)))	US-PGPUB; USPAT	OR	ON	2009/01/29 12:02
S289	34374	(fujitsu-lim$.as.)	US-PGPUB; USPAT	OR	ON	2009/01/29 12:00
S286	1	"20040073859"	US-PGPUB; USPAT	OR	ON	2009/01/29 12:00

S285	199	S283 S284	US-PGPUB; USPAT	OR	ON	2009/01/29 12:00
S284	3	(Tamiya-yut$.in.)	US-PGPUB; USPAT	OR	ON	2009/01/29 11:59
S283	197	(abe-Ken$.in.)	US-PGPUB; USPAT	OR	ON	2009/01/29 11:59
S282	430	((binary near2 decision near2 (graph diagram)) (binary near2 tree)) same (boolean)	US-PGPUB; USPAT	OR	ON	2008/04/25 17:26
S280	732	S279 not spectrum	US-PGPUB; USPAT	OR	ON	2008/04/25 15:39
S279	784	S278 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2008/04/25 15:36
S278	2700	(((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back?tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) same (input in-put in?put inputting in-putting in?putting inputs)) and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 15:35
S277	1	"5719881".pn.	US-PGPUB; USPAT	OR	ON	2008/04/25 15:21
S276	85	(((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back?tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) same (input in-put in?put inputting in-putting in?putting inputs) same (cone shaded)) and (test$4 near2 (pattern vector sequence	US-PGPUB; USPAT	OR	ON	2008/04/25 15:19

		data program script))				
S275	784	S274 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2008/04/25 15:05
S274	2700	((((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) same (input in-put in?put inputting in-putting in?putting inputs))and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 15:04
S273	1232	S272 and (input in-put in?put inputting in-putting in?putting) same (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 14:58
S272	2360	S271 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2008/04/25 14:57
S271	28412	(((back backward forward) near2 (trac$3 track$4)) (backtrace backtrack forwardtrace backwardtrace backtracing backtracking backwardtracing forwardtracing back-tracing back-tracking backward-tracing forward-tracing back?tracing back? tracking backward?tracing forward?tracing back?trace back?track forward?trace backward?trace back-trace back-track forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 14:56
S270	256	S269 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2008/04/25 14:37
S269	1479	S268 and S257	US-PGPUB; USPAT	OR	ON	2008/04/25 14:37

S268	27949	(((back backward forward) near2 trac$3) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing back?trace forward?trace backward?trace back-trace forward-trace backward-trace) (logic near2 path) (logic near2 group) spread spreading) and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 14:36
S264	1	"5592493".pn.	US-PGPUB; USPAT	OR	ON	2008/04/25 13:57
S263	80	S261 and S262	US-PGPUB; USPAT	OR	ON	2008/04/25 13:27
S262	107	S257 same path same (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 13:27
S261	1114	S259 and S260	US-PGPUB; USPAT	OR	ON	2008/04/25 13:24
S260	14895	((generat$4 creat$4) near2 (test $4 near2 (pattern vector sequence data program script)))	US-PGPUB; USPAT	OR	ON	2008/04/25 13:24
S259	2985	(S258 and path)	US-PGPUB; USPAT	OR	ON	2008/04/25 13:23
S258	5339	S257 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/25 13:16
S257	39786	((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)) (description near2 language) VHDL)	US-PGPUB; USPAT	OR	ON	2008/04/25 13:13
S256	1	"5592493".pn.	US-PGPUB; USPAT	OR	ON	2008/04/25 13:02
S255	1	"5831996".pn.	US-PGPUB; USPAT	OR	ON	2008/04/25 12:58
S254	1	"6385750".pn.	US-PGPUB; USPAT	OR	ON	2008/04/25 12:47
S253	1560	spreading same cone	US-PGPUB; USPAT	OR	ON	2008/04/23 17:04
S252	157	spreading and input and ouput	US-PGPUB; USPAT	OR	ON	2008/04/23 17:03
S251	33	(forward adj trace) same input same output	US-PGPUB; USPAT	OR	ON	2008/04/23 16:53
S250	184	(forward adj trace)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:53
S249	927274	(forward near2trace)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:52
S248	0	(forwardtrace)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:52

S247	0	(forwardtrace) same (input same output)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:52
S246	0	(forwadtrace) same (input same output)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:52
S245	0	(forwadtrace) same (input and output)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:52
S244	70	(backtrace) same input and output	US-PGPUB; USPAT	OR	ON	2008/04/23 16:45
S243	145	(backtrace)	US-PGPUB; USPAT	OR	ON	2008/04/23 16:44
S242	2491733	(((back backward forward) near2 trac$3) (trace tracing) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing) (logic near2 path) (logic group))	US-PGPUB; USPAT	OR	ON	2008/04/23 16:42
S241	5581	S240 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2008/04/23 16:38
S240	7395	S239 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2008/04/23 16:37
S239	23141	S238 and ("same time" together jointly simultaneous$3 parallal concurrent$3 simultaneity synchronic synchronous$3) with test$4	US-PGPUB; USPAT	OR	ON	2008/04/23 16:36
S238	119092	S232 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/23 16:34
S237	53	S226 same (simultaneous$3 parallal concurrent$3 simultaneity synchronic synchronous$3) same test$3 same S232	US-PGPUB; USPAT	OR	ON	2008/04/23 16:27
S236	1	"7266746".pn.	US-PGPUB; USPAT	OR	ON	2008/04/23 16:25
S235	97	S234 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2008/04/23 16:12
S234	258	S227 and S233	US-PGPUB; USPAT	OR	ON	2008/04/23 16:11
S233	5690	S222 same S232	US-PGPUB; USPAT	OR	ON	2008/04/23 16:11
S232	2611780	(((back backward forward) near2 trac$3) (trace tracing) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing) cone cones subgroup group groups (logic near2 path) (logic group))	US-PGPUB; USPAT	OR	ON	2008/04/23 16:10
S231	103	S230 and (test$4 near2 (pattern vector sequence data program script))	US-PGPUB; USPAT	OR	ON	2008/04/23 15:02

S230	156	S228 and S229	US-PGPUB; USPAT	OR	ON	2008/04/23 14:58
S229	109381	S217 same (group$4 set sets cluster collection number plurality bunch$3 band$3 match $4)	US-PGPUB; USPAT	OR	ON	2008/04/23 14:57
S228	327	S227 and S217	US-PGPUB; USPAT	OR	ON	2008/04/23 14:57
S227	1060	S222 and S226	US-PGPUB; USPAT	OR	ON	2008/04/23 14:56
S226	23455	(((net-list netlist net?list (net adj list)) (HDL (hardware near2 description near2 language)))	US-PGPUB; USPAT	OR	ON	2008/04/23 14:56
S225	1	"20040073859".pn.	US-PGPUB; USPAT	OR	ON	2008/04/23 14:43
S224	39	S223 and S219	US-PGPUB; USPAT	OR	ON	2008/04/23 14:43
S223	3028	S220 and S222	US-PGPUB; USPAT	OR	ON	2008/04/23 14:43
S222	48926	(simultaneous$3 parallal concurrent$3 simultaneity synchronic synchronous$3) with test$4	US-PGPUB; USPAT	OR	ON	2008/04/23 14:42
S220	105672	S217 same (group$4 set sets cluster collection number plurality bunch$3 band$3)	US-PGPUB; USPAT	OR	ON	2008/04/23 14:38
S219	417	S218 and test$4	US-PGPUB; USPAT	OR	ON	2008/04/23 14:35
S218	537	S216 same S217	US-PGPUB; USPAT	OR	ON	2008/04/23 14:35
S217	354754	(((back backward forward) near2 trac$3) (trace tracing) (backtrace forwardtrace backwardtrace backtracing backwardtracing forwardtracing))	US-PGPUB; USPAT	OR	ON	2008/04/23 14:34
S216	8154	(net-list netlist net?list (net adj list))	US-PGPUB; USPAT	OR	ON	2008/04/23 14:31
S215	201	S211 and S214	US-PGPUB; USPAT	OR	ON	2007/07/20 20:04
S214	27204	S212 S213	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 20:04
S212	3143	S209 with (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 20:04
S204	1260	(S178 with S179) and S181	US-PGPUB; USPAT	OR	ON	2007/07/20 20:04

S213	25265	S209 with ((cost importance value weight significan$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 20:03
S199	18623	S179 with ((cost importance value weight significan$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2007/07/20 20:03
S188	2025	S179 with (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2007/07/20 20:01
S211	1268	(S208 with S209) and S210	US-PGPUB; USPAT	OR	ON	2007/07/20 19:59
S210	59621	(netlist net-list (net adj list) (description near2 language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 19:57
S209	997785	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 19:57
S181	39991	(netlist net-list (net adj list) (description near2 language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT	OR	ON	2007/07/20 19:57
S179	555027	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/20 19:57
S208	34769	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 19:56
S178	25914	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/20 19:56
S207	28	S188 and S206	US-PGPUB; USPAT	OR	ON	2007/07/18 15:08
S206	1260	S204 or S205	US-PGPUB; USPAT	OR	ON	2007/07/18 15:07
S205	54	(S178 with S181) and S179	US-PGPUB; USPAT	OR	ON	2007/07/18 15:06
S203	25914	((S178 same S179) (S178 same S181)) and (S181 S179)	US-PGPUB; USPAT	OR	ON	2007/07/18 15:00

S201	106	S178 same S179 same S181	US-PGPUB; USPAT	OR	ON	2007/07/18 14:56
S138	1	S135 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/18 14:30
S200	193	S184 and S199	US-PGPUB; USPAT	OR	ON	2007/07/18 14:29
S195	28	S184 and S188	US-PGPUB; USPAT	OR	ON	2007/07/18 14:04
S149	195	S136 same (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/18 14:01
S184	1260	S183 and S181	US-PGPUB; USPAT	OR	ON	2007/07/18 13:58
S141	1	S140 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/18 13:58
S186	6072	S179 same (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2007/07/18 13:30
S187	75	S185 and S186	US-PGPUB; USPAT	OR	ON	2007/07/18 13:14
S185	428	S184 and (((cost effective) near7 (design circuit element unit device module)) (short$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2007/07/18 13:11
S183	25914	S178 with S179	US-PGPUB; USPAT	OR	ON	2007/07/18 13:10
S177	7	S176 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S176	50	S174 and (S128 same (cost importance weight significan$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S170	22	S169 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S169	197	S166 and (S128 same (cost importance value weight significan$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S175	204	S174 and (cost importance weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S174	269	S161 and S173	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S168	283	S166 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S166	299	S161 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34

S173	219930	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) with (cost importance (new near2 design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:33
S128	257750	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) with (cost importance value (new near2 design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:33
S159	841	S139 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:32
S167	1	S166 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/17 19:28
S151	12	S150 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:28
S162	107	S161 same S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:27
S160	525	S139 and (S128 same (cost importance value weight significan$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:27
S161	434	S134 with S124	US-PGPUB; USPAT	OR	ON	2007/07/17 19:25
S136	381	S135 same S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:25
S135	1218	S134 same S124	US-PGPUB; USPAT	OR	ON	2007/07/17 19:25
S148	374	S136 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:23
S152	0	S149 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/17 19:22
S153	374	S136 and (cost importance value weight significan$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:21
S150	127	S149 and @ad<="20021009"	US-PGPUB; USPAT	OR	ON	2007/07/17 19:20
S140	87	S139 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:20
S137	29	S136 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:17
S139	874	S135 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:00

S133	29	S132 and (("714"/$.ccls.) ("324"/$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:58
S132	381	S130 same S128	US-PGPUB; USPAT	OR	ON	2007/07/17 18:58
S130	1218	S122 same S124	US-PGPUB; USPAT	OR	ON	2007/07/17 18:58
S134	4796822	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog (funcational adj configuration))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:57
S122	4796822	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT	OR	ON	2007/07/17 18:57
S131	874	S130 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 18:54
S129	17422	S125 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 18:54
S125	25629	S122 and S124	US-PGPUB; USPAT	OR	ON	2007/07/17 18:54
S124	25914	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:49
S123	41405	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:45
S97	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:44
S121	4	S120 and (@ad< = "20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S120	9	(test adj equipment adj platform)	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S118	871	S117 and (@ad< = "20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S117	1256	TEP (test adj equipment adj platform)	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S119	8	S118 and ("714"/$.ccls.)	US-PGPUB; USPAT	OR	ON	2007/07/16 20:17

S112	880	(environment near3 (operation operating operational normal funcational)) and ((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) and ("714"/$.ccls.) and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 20:17
S116	36	S113 and S99	US-PGPUB; USPAT	OR	ON	2007/07/16 20:16
S115	98	S113 and S98	US-PGPUB; USPAT	OR	ON	2007/07/16 19:48
S113	161	S112 and S97	US-PGPUB; USPAT	OR	ON	2007/07/16 19:48
S111	1842	(environment near3 (operation operating operational normal funcational)) and ("714"/$.ccls.) and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 19:46
S108	6	("6256759" "6385750" "5450414" "5592493" "6708305" "5831996").pn.	US-PGPUB; USPAT	OR	ON	2007/07/16 19:43
S110	28831	(test$4 verif$4) with (new$2 additional added) with (unit chip device module circuit)	US-PGPUB; USPAT	OR	ON	2007/07/16 18:52
S109	1	"20020124217".pn.	US-PGPUB; USPAT	OR	ON	2007/07/16 18:48
S107	58	S103 and ((test$4 verif$4 validat$4 cost magnitude) near2 (cost weight priority control$3 observation))	US-PGPUB; USPAT	OR	ON	2007/07/16 18:39
S105	47	S103 and ((test$4 verif$4 validat$4) near2 (cost weight priority control$3 observation))	US-PGPUB; USPAT	OR	ON	2007/07/16 17:30
S106	375	S103 and (cost priority control $3 observation)	US-PGPUB; USPAT	OR	ON	2007/07/16 17:28
S104	375	S103 and (cost weight priority control$3 observation)	US-PGPUB; USPAT	OR	ON	2007/07/16 17:28
S103	389	S102 and S101	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S102	16989	S97 and S98	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S101	389	(function program algorithm equation code) same S100	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S100	1216	S97 same S99	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S99	25887	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20

EAST Search History

S98	119899	(requirement condition$3 limit $5 criteria rule) same (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S96	79	validation adj cost	US-PGPUB; USPAT	OR	ON	2007/07/16 17:19
S95	375	S94 and (cost weight priority control$3 observation)	US-PGPUB; USPAT	OR	ON	2007/07/16 17:19
S94	389	S93 and S81	US-PGPUB; USPAT	OR	ON	2007/07/16 15:53
S93	16989	S68 and S69	US-PGPUB; USPAT	OR	ON	2007/07/16 15:47
S71	7309	S68 same S69	US-PGPUB; USPAT	OR	ON	2007/07/16 15:47
S92	29	S89 and priority	US-PGPUB; USPAT	OR	ON	2007/07/16 15:38
S89	198	S88 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/16 15:38
S91	21	S71 and S89	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S90	81	S68 and S89	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S88	267	714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S87	27	(S71) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S78	224	S77 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S86	3	(S81 S77) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 13:33
S85	3	(S82 S79) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 13:31
S84	14	(S82 S79) and simulation	US-PGPUB; USPAT	OR	ON	2007/07/16 13:29
S83	16	S82 not S79	US-PGPUB; USPAT	OR	ON	2007/07/16 13:13
S82	35	S81 and (("714"/$.ccls.) ("324"/ $.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/16 13:01
S79	19	S77 and (("714"/$.ccls.) ("324"/ $.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/16 13:01
S81	389	(function program algorithm equation code) same S76	US-PGPUB; USPAT	OR	ON	2007/07/16 13:00
S77	311	(function program algorithm) same S76	US-PGPUB; USPAT	OR	ON	2007/07/16 12:59

S80	14	("4204633" "4779273" "5390193" "5657240" "5726996" "5831996" "5862149" "5896401" "6215327" "6442722" "6463560" "6467058" "6477684" "6611933").pn.	US-PGPUB; USPAT	OR	ON	2007/07/16 12:53
S76	1216	S68 same S75	US-PGPUB; USPAT	OR	ON	2007/07/16 10:41
S73	2479	(function program algorithm) same S71	US-PGPUB; USPAT	OR	ON	2007/07/16 10:41
S75	25887	(requirement condition$3 limit $5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:40
S74	52106	(requirement condition$3 limit $5 criteria rule) with (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:40
S69	119899	(requirement condition$3 limit $5 criteria rule) same (TP (test adj (pattern sequence vector data)) ((input output input/ output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:40
S68	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:27
S67	24	S49 same ((ciritical adj point) (observation adj point) ((target specific particular) near2 (circuit chip module divice unit apparatus)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:25
S65	327	S49 and ((ciritical adj point) (observation adj point) ((target specific particular) near2 (circuit chip module divice unit apparatus)))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:45
S66	262	S65 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:33
S64	437	S49 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:33
S61	257	S60 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:31

S63	1	"20050149804".pn.	US-PGPUB; USPAT	OR	ON	2007/07/12 18:26
S39	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:26
S62	21	S49 same ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:21
S60	320	S49 and ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:21
S50	11135	(test$4 verif$4) with ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:20
S51	111	S49 and S50	US-PGPUB; USPAT	OR	ON	2007/07/12 18:17
S54	34	S53 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:08
S53	47	S49 and (BDD (binary near2 (decision determination determin$3 solution)) (state adj diagram))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:08
S49	552	(netlist net-list (net adj list) (description adj language) HDL HTL RTL VHDL verilog) with (((test adj (pattern sequence vector)) (((input near2 output) input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 17:48
S52	84	S51 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 16:47
S48	36	S47 same ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:45
S47	1643	(netlist net-list (net adj list) (description adj language) HDL HTL RTL VHDL verilog) with (((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:43
S44	120	S42 same ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:16

S42	12632	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) with (((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:15
S46	38	S44 and priority	US-PGPUB; USPAT	OR	ON	2007/07/12 16:13
S45	0	S44 same priority	US-PGPUB; USPAT	OR	ON	2007/07/12 16:13
S43	3379	S42 and ((target specific particular) near2 (circuit chip module divice unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:13
S41	988	S39 same ((condition limit$4 constraint paramiter provision circumstances circumstanc requirement$4 limitation restraint) near5 (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data))))	US-PGPUB; USPAT	OR	ON	2007/07/12 14:39
S40	4352	S39 and ((condition limit$4 constraint paramiter provision circumstances circumstanc requirement$4 limitation restraint) near5 (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data))))	US-PGPUB; USPAT	OR	ON	2007/07/12 14:39
S35	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 14:28
S38	1	"5542043".pn.	US-PGPUB; USPAT	OR	ON	2007/07/12 14:15
S37	1	"5870590".pn.	US-PGPUB; USPAT	OR	ON	2007/07/12 14:15
S36	12310	S35 and (binary)	US-PGPUB; USPAT	OR	ON	2007/07/12 14:15
S2	1	"20040073859"	US-PGPUB; USPAT	OR	ON	2007/07/12 13:56
S34	5	S30 and ((test test$4 verify verif$4) near2 (pattern sequence vector))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:55
S32	159	(validate validating validation) and (functional near2 device) and ((test test$4 verify verif$4) near2 (pattern sequence vector))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:55

S30	30	(((functional configuration) near2 (information data)) same (condition)) and (validation near2 (item unit circuit module chip device))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:55
S33	41	S32 and ((resource device circuit element) near2 (constraint restriction limitation constrain$4 restrict$4 limit$4))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:53
S31	698	(((functional configuration) near2 (information data)) and (condition)) and (validation near2 (item unit circuit module chip device))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:40
S29	149990	(configuration same condition)	US-PGPUB; USPAT	OR	ON	2007/07/06 15:15
S5	2	("5542043" "5870590").pn.	USPAT	OR	ON	2007/07/06 15:11
S28	1	"6578167".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 19:21
S7	0	"08658344".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 19:21
S26	1253	S24 and ((scan same (figure diagram picture graph)) same (input data) same (edge edges node nodes))	US-PGPUB; USPAT	OR	ON	2006/10/17 16:58
S27	140	S26 and "714"/$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/17 16:53
S25	33230	S24 and (scan same (figure diagram picture))	US-PGPUB; USPAT	OR	ON	2006/10/17 16:52
S24	3962126	@ad<="20021009"	US-PGPUB; USPAT	OR	ON	2006/10/17 16:50
S6	3959421	@ad<="20021009"	US-PGPUB; USPAT	OR	ON	2006/10/17 16:49
S23	0	"19990318204".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 15:04
S22	0	"9990318204".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 15:04
S21	1	"5394347".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 15:04
S20	1	"6694290".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 15:04
S19	1	"5870590".pn.	US-PGPUB; USPAT	OR	ON	2006/10/17 15:03
S18	1	"6631340".pn.	US-PGPUB; USPAT	OR	ON	2006/09/28 14:04
S17	83	S15 and "714"/$.ccls.	US-PGPUB; USPAT	OR	ON	2006/09/28 12:31
S16	838	S15 and (condition$3 limit$4)	US-PGPUB; USPAT	OR	ON	2006/09/28 10:52
S12	261	S11 and "714"/$.ccls.	US-PGPUB; USPAT	OR	ON	2006/09/28 10:52

S15	838	S14 and (condition$3 limit$4)	US-PGPUB; USPAT	OR	ON	2006/09/28 10:51
S14	859	S13 and (known near3 (good working)) and (together assemble build assemble) and (test check) and (simulat$4)	US-PGPUB; USPAT	OR	ON	2006/09/28 10:51
S13	3959437	@ad<="20021009"	US-PGPUB; USPAT	OR	ON	2006/09/28 10:51
S11	5164	S10 and (condition$3 limit$4)	US-PGPUB; USPAT	OR	ON	2006/09/28 10:51
S10	5452	S6 and (known near3 (good working)) and (together assemble build assemble) and (test simulate check)	US-PGPUB; USPAT	OR	ON	2006/09/28 10:50
S9	1	"5918037".pn.	US-PGPUB; USPAT	OR	ON	2006/09/27 21:49
S8	0	"8658344".pn.	US-PGPUB; USPAT	OR	ON	2006/09/27 20:53
S3	1	"20020138802"	US-PGPUB; USPAT	OR	ON	2006/01/30 12:18

1/29/2009 3:08:02 PM
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